Embedded Systems Programming - PA8001

http://bit.ly/15mmqf7

Lecture 5

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Concurrency

```
struct Params params;
```

```
void controller_main() {
  int dist, signal;
  while(1){
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    control(dist,
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Providing means for two mains to execute concurrently! As if we had 2 CPUs!

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Concurrent programming

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main(){
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Notice that spawn takes a *function* as an argument.

We also need to interleave the threads.

spawn: provides an extra Program Counter and Stack Pointer

yield: seizing control to another thread

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Calling yield()

Explicitly

```
ld a, r1
ld b, r2
add r, r2
st r2, c
jsr yield
ld c, r0
cmp #37, r0
ble label34
...
```

```
yield:
    sub #2, sp
    ...
    mov #0, r0
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  ⟵  Interrupt on pin 3!

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    push r0-r2
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    rti

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Installing interrupt handlers

```
#include<avr/interrupt.h>

...
ISR(interrupt_name){
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 // code as in a function body!
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}
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Preventing interrupts in avr-gcc

```
cli();
// ... code that must not be interrupted ...
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Preventing interrupts

Why should we consider disabling interrupts? What parts of the program should be protected?

The critical section problem

What if `params` is read (by the controller) at the same time as it is written (by the decoder)?

I.e., what if the scheduler interleaves read and write instructions from the controller and the decoder?

Mutual exclusion: a central issues in concurrency.

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Mutual exclusion: a central issues in concurrency.

Our embedded system

```
struct Params p;
```

```
while(1){
   ...
   p.minDistance = e1;
   p.maxSpeed = e2;
}
```

```
while(1){
   local_minD = p.minDistance;
   local_maxS = p.maxSpeed;
   ...
}
```

Possible interleaving

```
p.minDistance = 1;
p.maxSpeed = 1;
                                    local_minD = 1;
p.minDistance = 200;
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```

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The classical solution

Apply an access protocol to the critical sections that ensures mutual exclusion

Require that all parties follow the protocol

Access protocols are realized by means of a shared datastructure known as a mutex or a lock.

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Mutual exclusion

```
struct Params p;
mutex m;
```

```
while(1){
    ...
    lock(&m);
    p.minDistance = e1;
    p.maxSpeed = e2;
    unlock(&m);
}
```

```
while(1){
    lock(&m);
    local_minD = p.minDistance;
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    unlock(&m);
    ...
}
```

The datatype `mutex` and the operations `lock` and `unlock` are defined in the kernel: each mutex has a queue of threads that are not in the ready queue. The operations move threads to and from the ready queue!

What we have learned . . .

- We know how to read and write to I/O device registers
- We know how to run several computations in parallel by time-slicing the CPU
- We know how to protect critical sections by means of a mutex

But . . .

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Still not satisfied!

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⟵ Time slicing ⟶

Each thread gets half of the CPU cycles, irrespective of whether it is waiting or computing!

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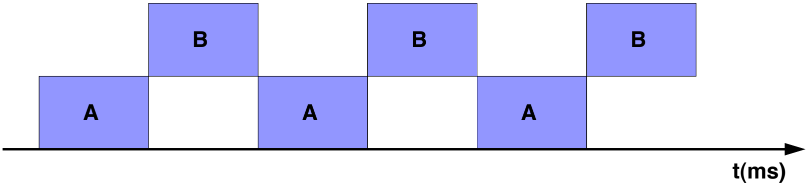
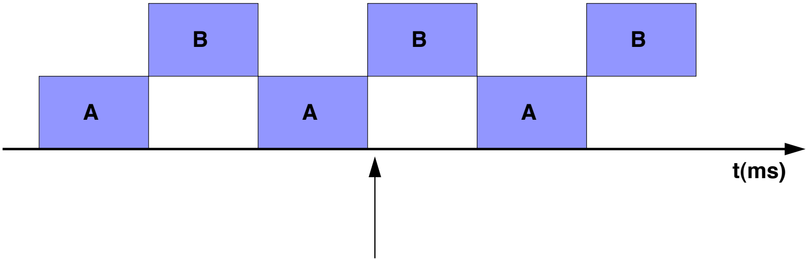


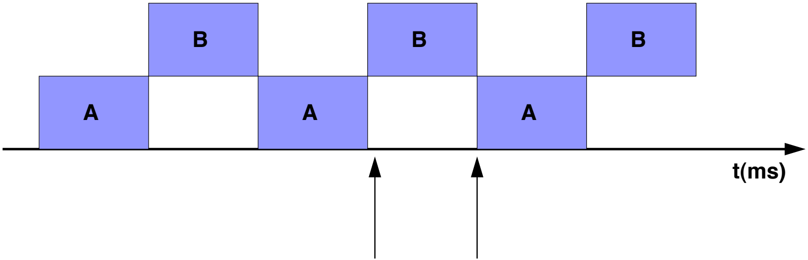
Say each thread gets **T**ms for execution, both waiting and computing!

Consequence 1



Say that an event that **A** is waiting for occurs now . . .

Consequence 1

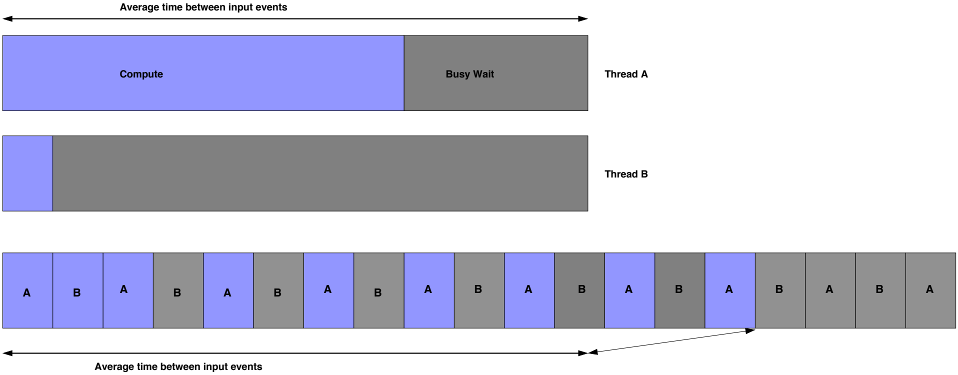


. . . it will not be noticed until now!

Consequence 1

With **N** threads in the system, each getting **T**ms for execution, a status change might have to wait up to **T*(N-1)**ms to be noticed!

Consequence 2



Busy waiting makes waiting indistinguishable from computing.
Thread A cannot keep up with event rate!

Busy waiting and Time slicing

Minus ...

1. Not a satisfactory technique for input synchronization if the system must meet real-time constraints!
2. Not a satisfactory technique for a system that is battery driven: 100% CPU cycle usage (100% power usage!).

Could we do otherwise?

An input synchronization technique that does not require the receiver of data to actively ask whether data has arrived.

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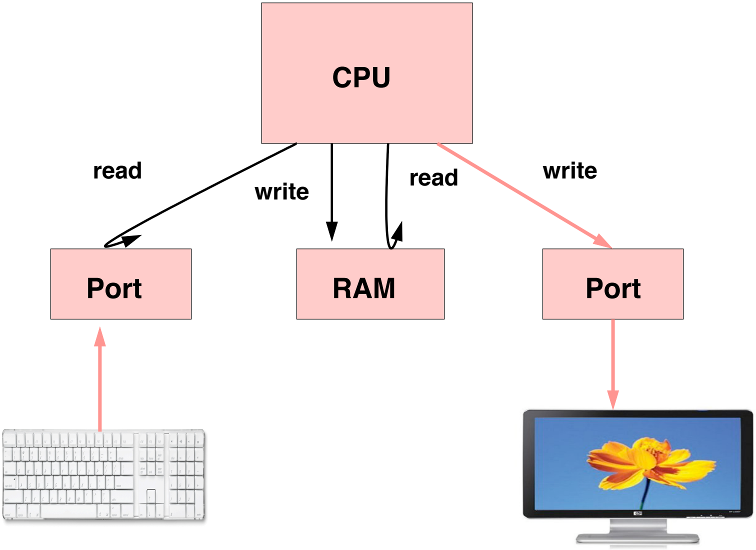
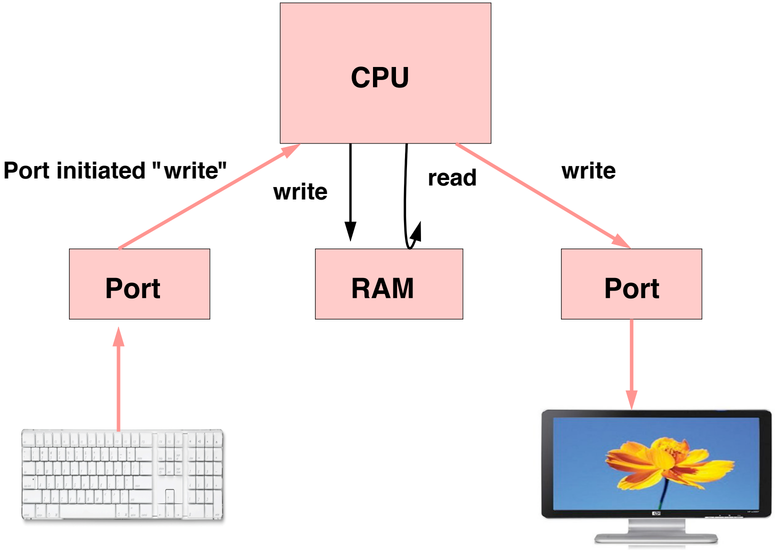
The naked computer – a mismatch



The naked computer – alternative



An analogy

You are expecting delivery of your latest web-shop purchase

Busy waiting

Go to the post-office again and again to check if the delivery has arrived.

Reacting to an interrupt

Receive a note in your mailbox that the goods can be picked up.

The CPU reacts to an interrupt signal by executing a designated ISR (interrupt service routine)

This has consequences for the way we structure programs. They become inside-out!

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ISRs vs functions

Busy waiting

We defined functions like `sonar_read` that can be called in the program. The CPU decides when to call the function:

```
while(1){
   sonar_read();
   control();
}
```

Input detection = the exit from the busy waiting fragment (a function return)

Reacting

We define ISRs. These are not called from the program, but the code is executed when an interrupt occurs:

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Two ways of organizing programs

CPU centric
One thread of control that runs from start to stop (or forever) reading and writing data as it goes.

Reacting CPU
A set of code fragments that constitute the reactions to recognized events.

The main part of the course from now on will focus on the reactive view.

Two ways of organizing programs

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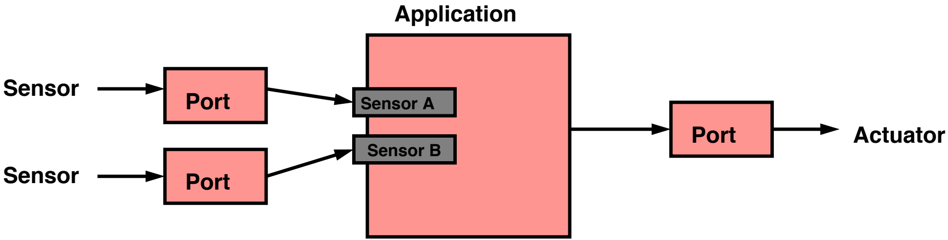
The reactive embedded system



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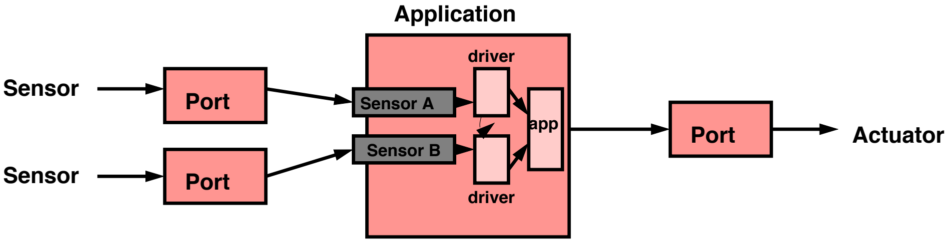

Reactive Objects

Boxes

Represent software or hardware reactive objects that:

- Maintain an internal state (variables, registers, etc)
- Provide a set of methods as reactions to external events (ISRs, etc)
- Simply rest between reactions!

Arrows

Represent event or signal or message flow between objects that can be either

- synchronous
- asynchronous

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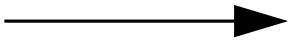
- asynchronous 
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Hardware objects

Hardware devices are reactive objects

A black box that does nothing unless stimulated by external events.

Serial port - state

Internal registers

Serial port - stimuli

- Signal change
- Bit pattern received
- Clock pulse

Serial port - emissions

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- Interrupt signal

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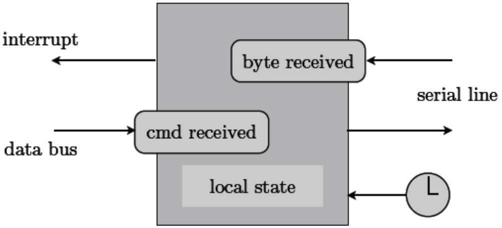

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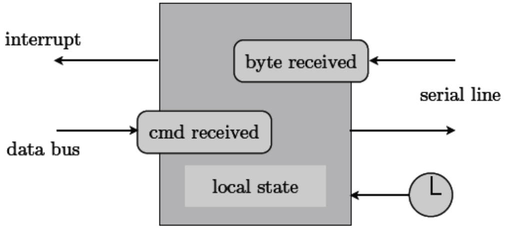

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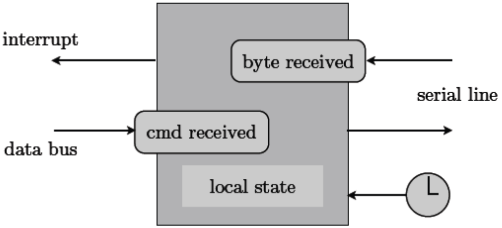

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We would like to regard software objects as reactive objects . . .

The Counter example

```
class Counter{
  int x;
  Counter(){x=0;}
  void inc(){x++;}
  int read(){return x;}
  void reset(){x=0;}
  void show(){
     System.out.print(x);}
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`print()` to the object `System.out`

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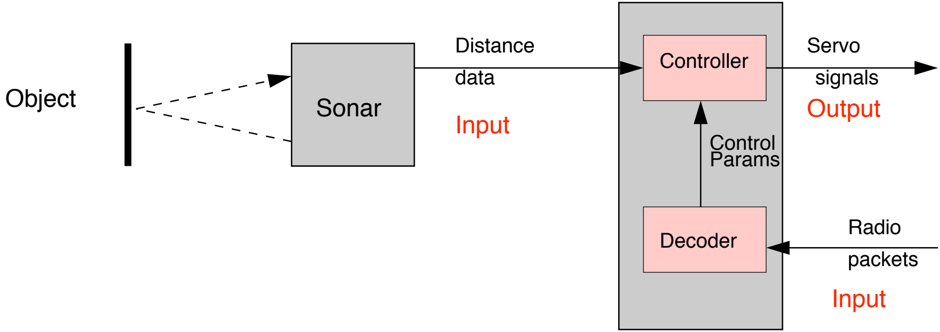
Counter - stimuli

`inc()`, `read()`, `reset()`, `show()`

Counter - emissions

`print()` to the object `System.out`

Back to our running example



All messages/events are asynchronous! Either generated by the CPU or by the sonar hw or by the communication hardware.

Reactive Objects

Object Oriented Programming?

- Objects have local state
- Objects export methods
- Objects communicate by sending messages
- Objects rest between method invocation

Examples of intuitive objects

People, cars, molecules, . . .

Bonus

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Java? C++?

The Counter example again

```
class Counter{
    int x;
    Counter(){x=0;}
    void inc(){x++;}
    int read(){return x;}
    void reset(){x=0;}
}
```

One thread

```
public static void main(){
  Counter c = new Counter();
  c.inc();
  System.out.println(c.read());
}
```

Creating a new object just creates a passive piece of storage! Not a thread of control!

Other threads that use the same counter are sharing the state!

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OO and Concurrency

OO Languages:

- An object is a passive piece of global state
- A method is a function
- Sending a message is calling a function

Our model says

- An object is an independent process with local state
- A method is a process fragment
- Sending a message is interprocess communication

This is one of the reasons why we choose to build our own kernel supporting reactive objects and programming in C.

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Reactive objects in C

We will need to provide ways for

- Create reactive objects
- Declare protected local state
- Receive messages
  - synchronously
  - asynchronously
- Bridge the hardware/software divide (run ISRs)
- Schedule a system of reactive software objects.

This will be the contents of a kernel called TinyTimber that we will learn how to design and use!

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Hardware objects

Black boxes that do nothing unless stimulated by external events.

Class
The kind or type or model of a circuit.

Instance
A particular circuit on a particular board.

State
Internal register status or logic status of an object instance.

Provided interface
The set of pins on a circuit that recognize signals.

Required interface
The set of pins on a circuit that generate signals.

Method call
To raise an input signal and wait for a response (synchronous) or just continue (asynchronous).

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Class
Program behaviour expressed as state variable layout and method code.

Instance
A record of state variables at at a particular address (the object's identity).

State
Current state variable contents of a particular object.

Provided interface
The set of methods a class exports.

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Method calls issued to other objects.

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Call to a function with the designated object address as the first argument.

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Encoding state layout

We will use a little kernel called `TinyTimber`. We will use files as modules in C.

In `MyClass.h`

```
#include "TinyTimber.h"

typedef struct{
    Object super;
    int x;
    char y;
} MyClass;

#define initMyClass(z) \
  { initObject ,0,z}
```

- Mandatory! Specified and used by the kernel!
- Unconstrained!
- `initMyClass` corresponds to a constructor, it includes programmer defined initialization.

Using it

```
#include "MyClass.h"
MyClass a = initMyClass(13);
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Comparing with Java

```
class MyClass{
   int x;
   char y;
   MyClass(int z){
      x=0;
      y=z;
   }
}
```

In our programs we do not allocate objects in the heap (as Java does!).

Our constructors are just preprocessor macros!

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MyClass a = new MyClass(13);
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int myMethod( MyClass *self , int q);
```

In MyClass.c

```
int myMethod( MyClass *self , int q){
  self -> x  =  self -> y  + q;
}
```

In Java

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In Java

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...
MyClass a = new MyClass(13);
a.myMethod(44);
```

In our C programs

```
...
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But, we are doing all this to do something different than just function calls! We want to have the possibility of introducing the distinction between synchronous and asynchronous messages!

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