



# The ioco Theory for Model-Based Testing with Labelled Transition Systems

Jan Tretmans

jan.tretmans@tno.nl



The ioco Theory for Model-Based Testing Overview

- The Models LTS
- Comparing LTS
  - equivalences
- Correctness
  - implementation relations
  - ioco

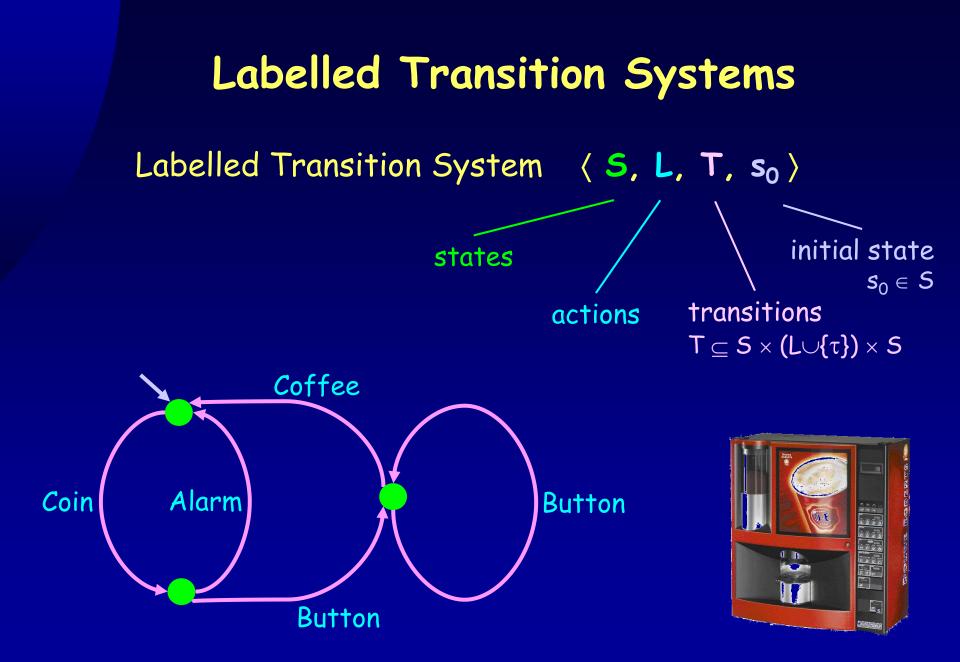
Testing LTS

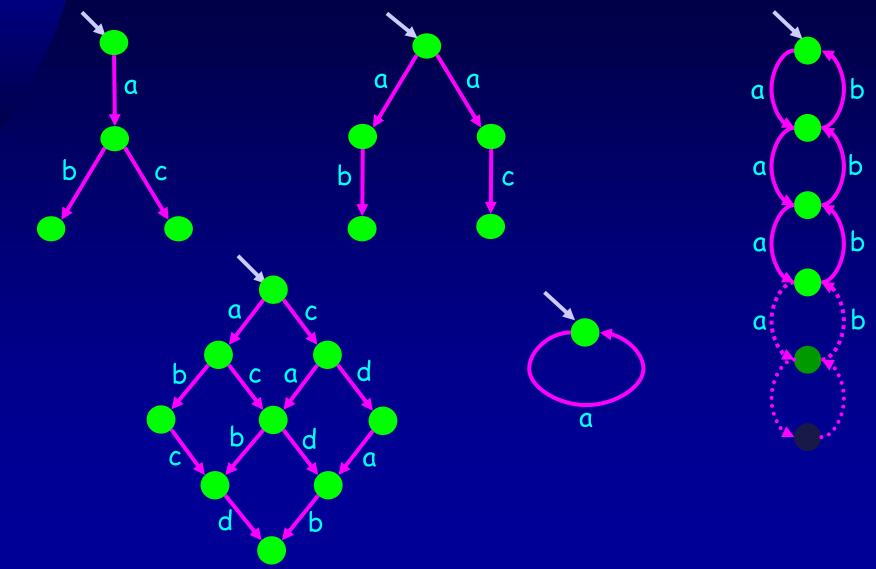
- test generation
- test execution
- Correctness & Testing
  - soundness
  - exhaustiveness
- SUT: Black-Box & Formal
  - test assumption

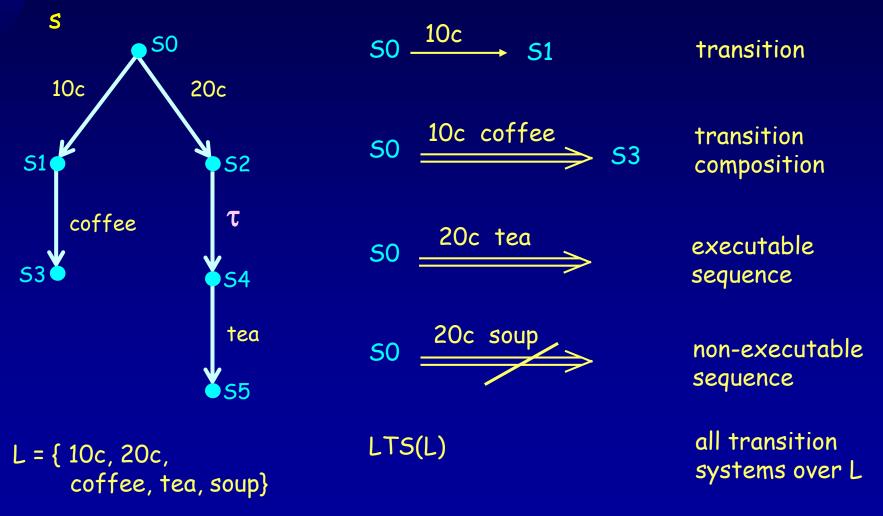




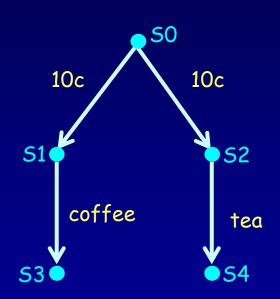
3







© Jan Tretmans



5

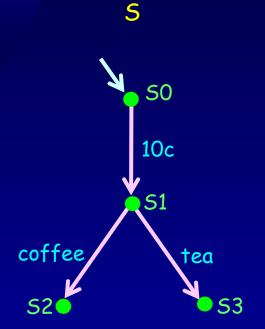
Sequences of observable actions:

traces(s) = {  $\sigma \in L^* \mid s \stackrel{\sigma}{\Longrightarrow}$  } traces(s) = {  $\epsilon$ , 10c, 10c coffee, 10c tea }

Reachable states:

s after  $\sigma = \{ s' \mid s \xrightarrow{\sigma} s' \}$ s after 10c =  $\{ s1, s2 \}$ s after 10c tea =  $\{ s4 \}$ 

### **Representation of LTS**



Explicit :

{ {\$0,\$1,\$2,\$3},

{10c,coffee,tea},

{ (\$0,10c,\$1), (\$1,coffee,\$2), (\$1,tea,\$3) },

S0 >

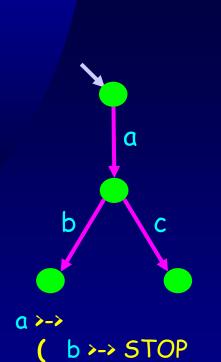
Transition tree / graph

Language / behaviour expression :

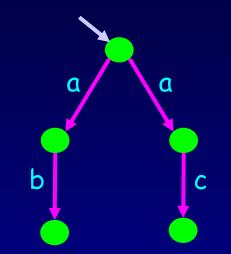
S ::=

10c >-> ( coffee >-> STOP ## tea >-> STOP )

## **Representation of LTS**



c >-> STOP



a >-> b >-> STOP ## a >-> c >-> STOP

a >-> b >-> STOP ||| c >-> d >-> STOP

)

##

d

α

b

C

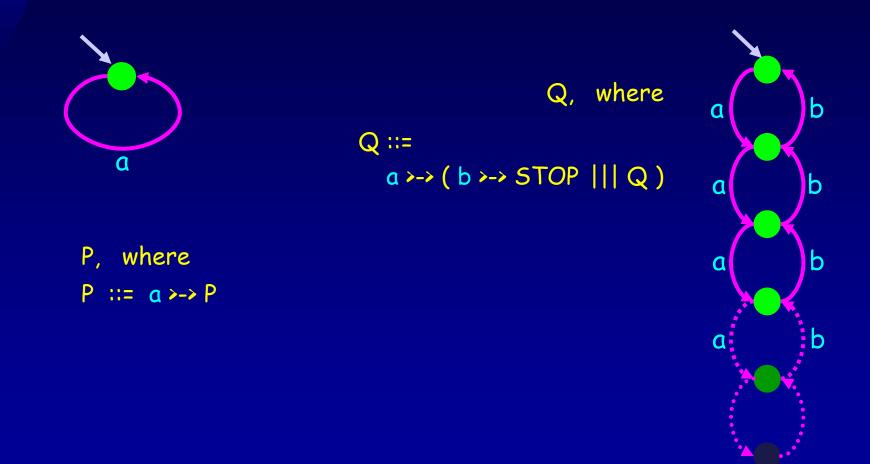
b

С

a

D

# Representation of LTS

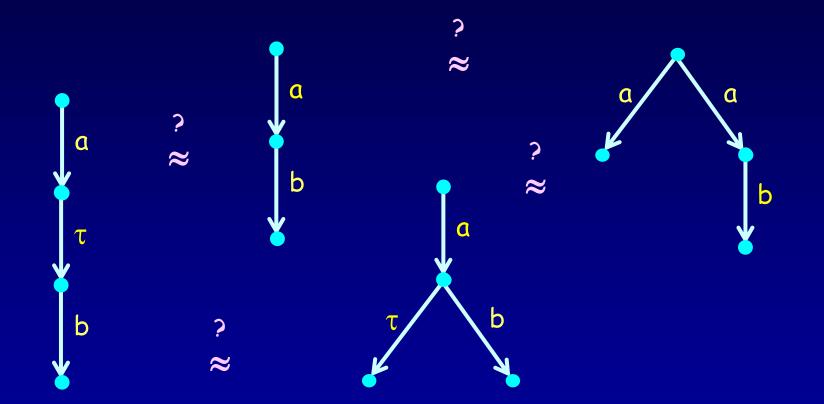






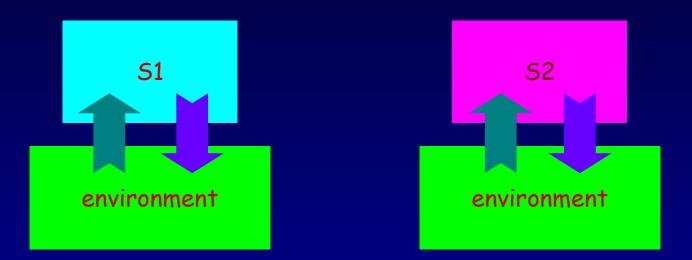
# Equivalences on Labelled Transition Systems

### **Observable Behaviour**



" Some transition systems are more equal than others "

### **Comparing Transition Systems**

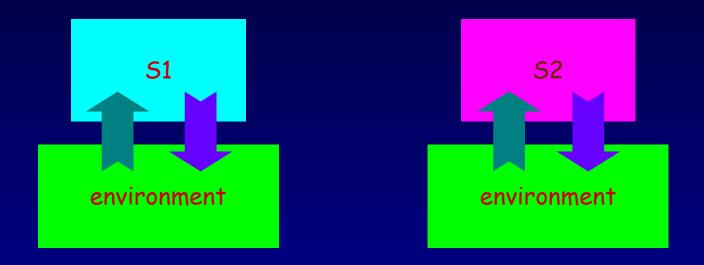


Suppose an environment interacts with the systems:

- the environment tests the system as black box by observing and actively controlling it;
- the environment acts as a tester;

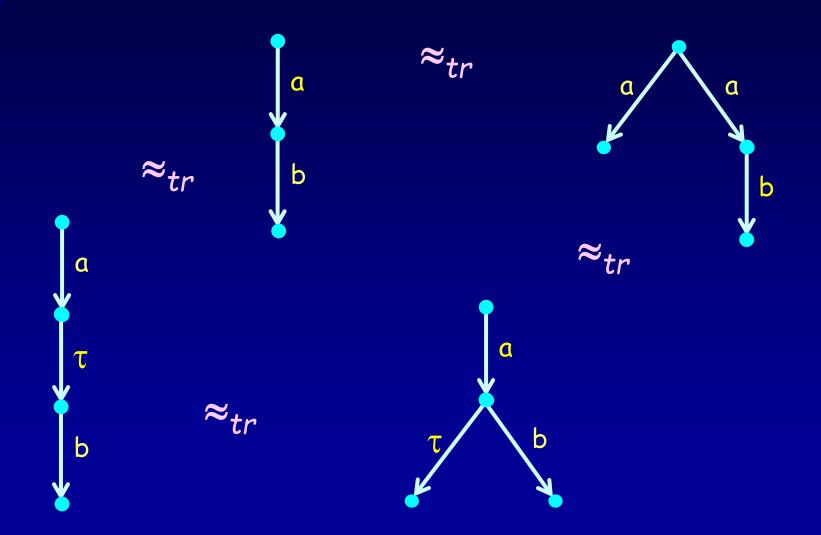
Two systems are equivalent if they pass the same tests.

### Trace Equivalence

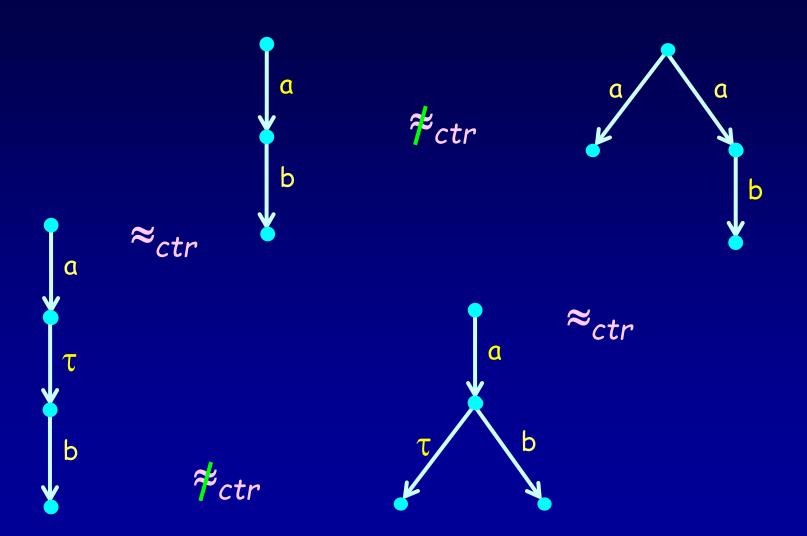


# S1 $\approx_{tr}$ S2 $\Leftrightarrow$ traces (S1) = traces (S2) Traces: traces (S) = { $\sigma \in L^* \mid S \xrightarrow{\sigma}$ }

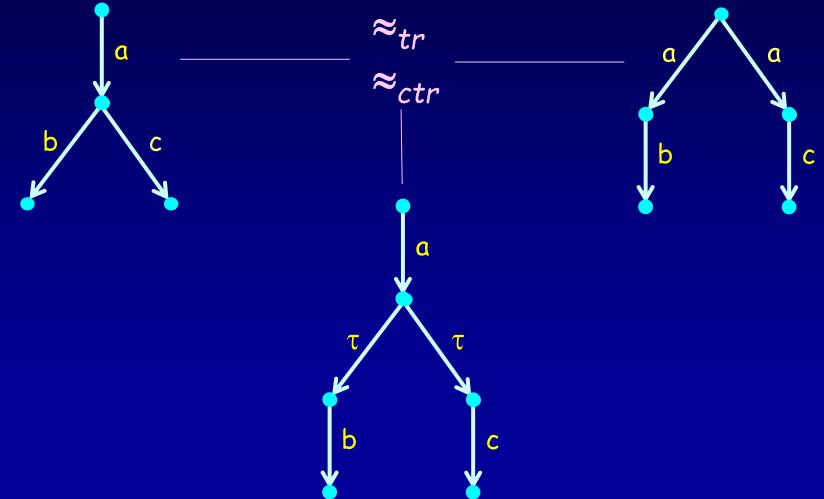
# Trace Equivalence



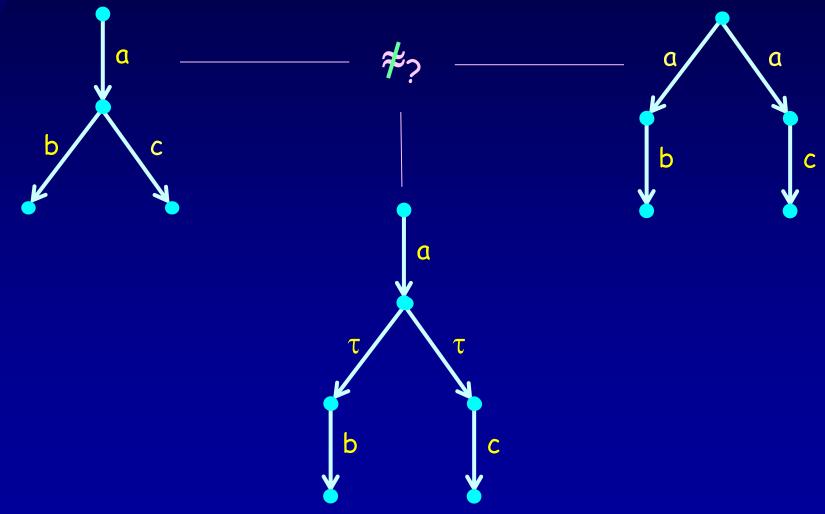
## **Completed Trace Equivalence**



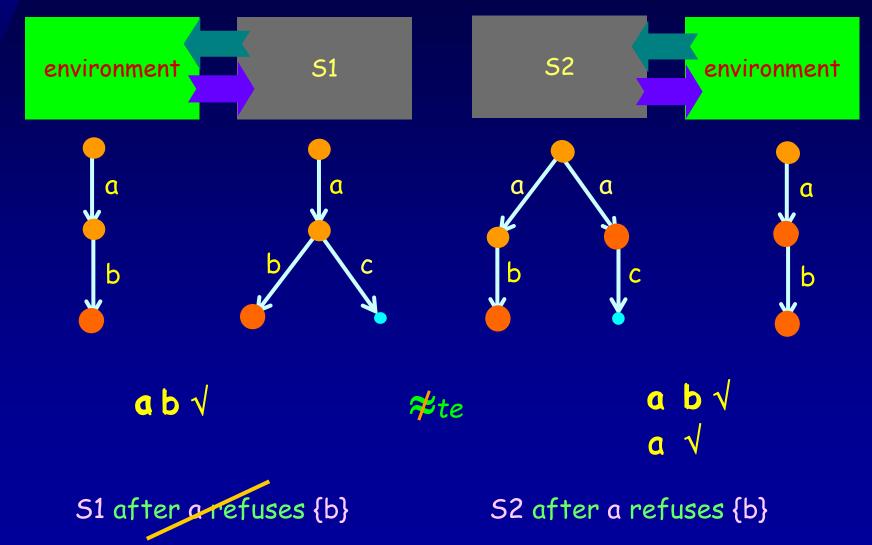
# (Completed) Trace Equivalence



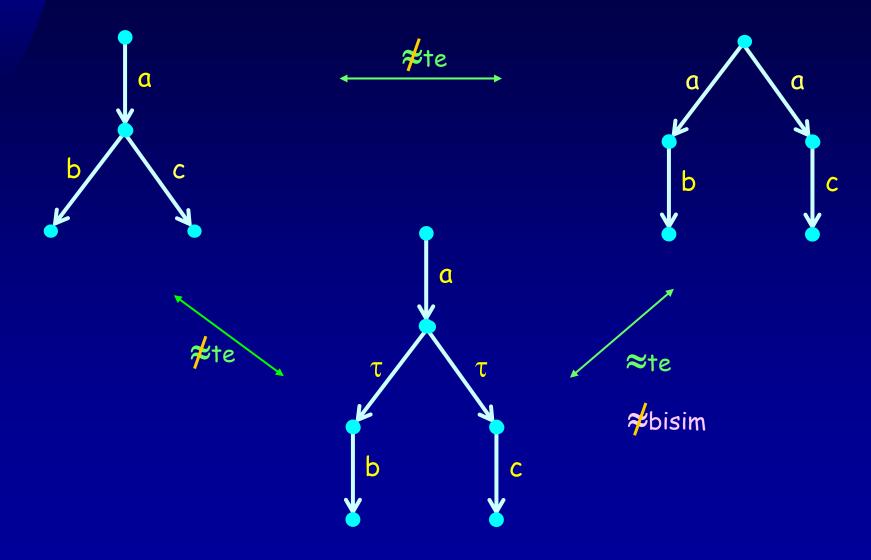
# (Completed) Trace Equivalence : Others ?



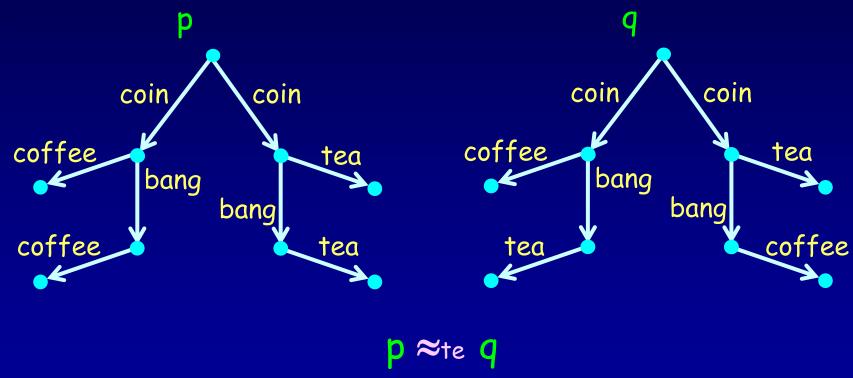
## Comparing Systems : Testing Equivalence







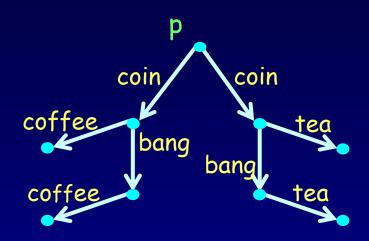
## **Testing Equivalence**

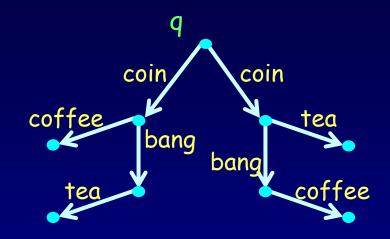


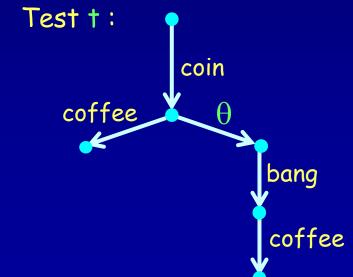
#### But:

if you want coffee you will eventually always succeed in q but not p !?

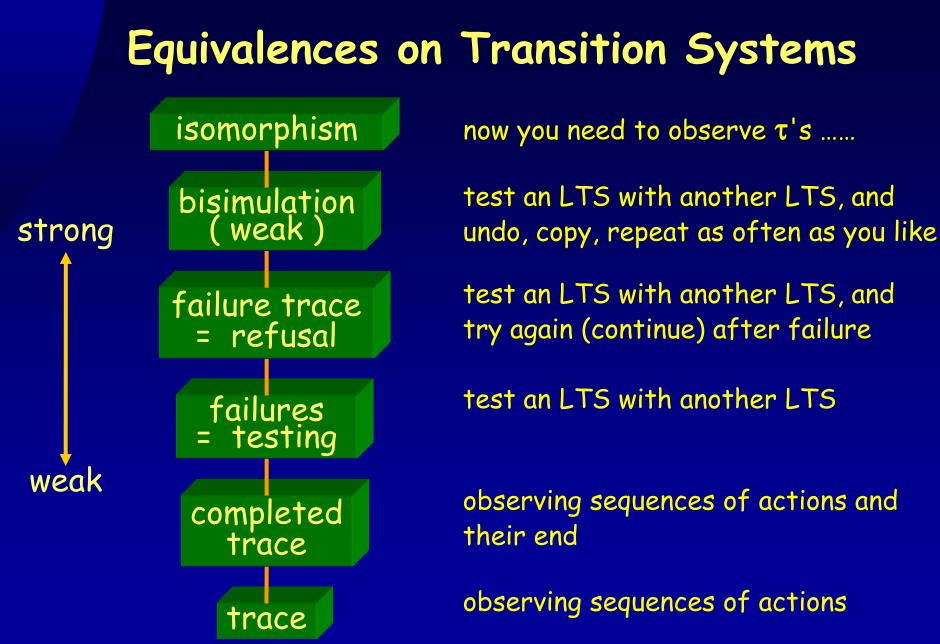
### **Refusal Equivalence**



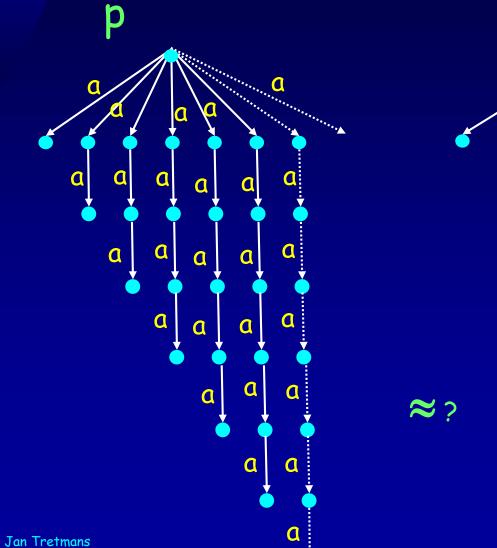




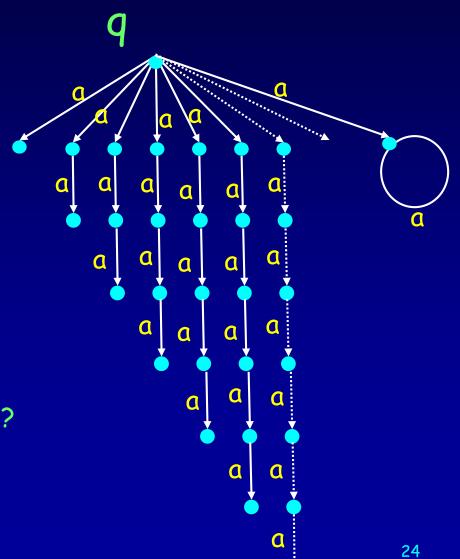
θ only possible
if nothing else is possible
coin θ bang coffee √ ∉ obs (p || +)
coin θ bang coffee √ ∈ obs (q || +)



# Equivalences : Examples



**©** 



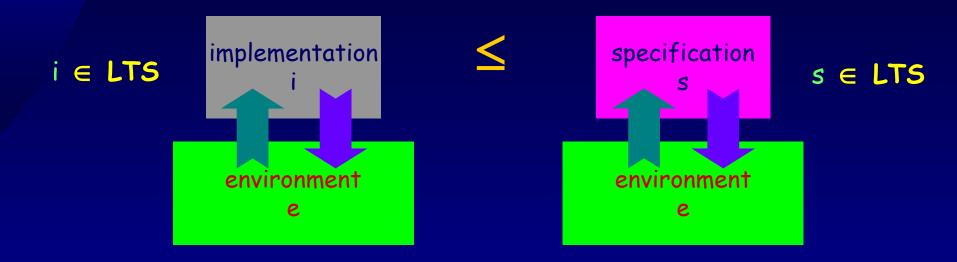




# Non-Equivalence Relations on Labelled Transition Systems

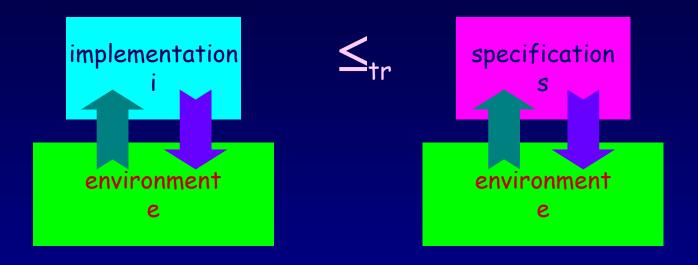
Implementation Relations Conformance Relations Refinement Relations Pre-Orders

## **Preorders on Transition Systems**



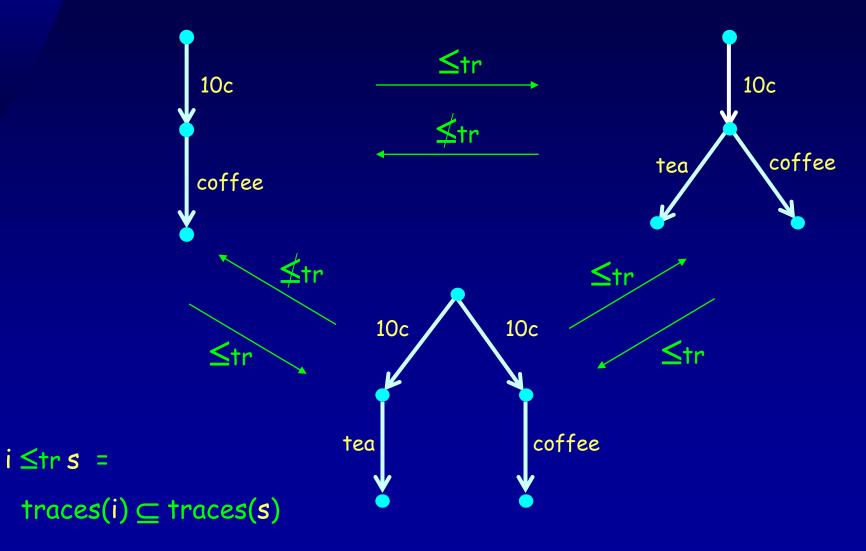
- Suppose an environment interacts with the black box implementation i and with the specification s:
  - i correctly implements s
    - if all observation of i can be related to observations of s

### Trace Preorder

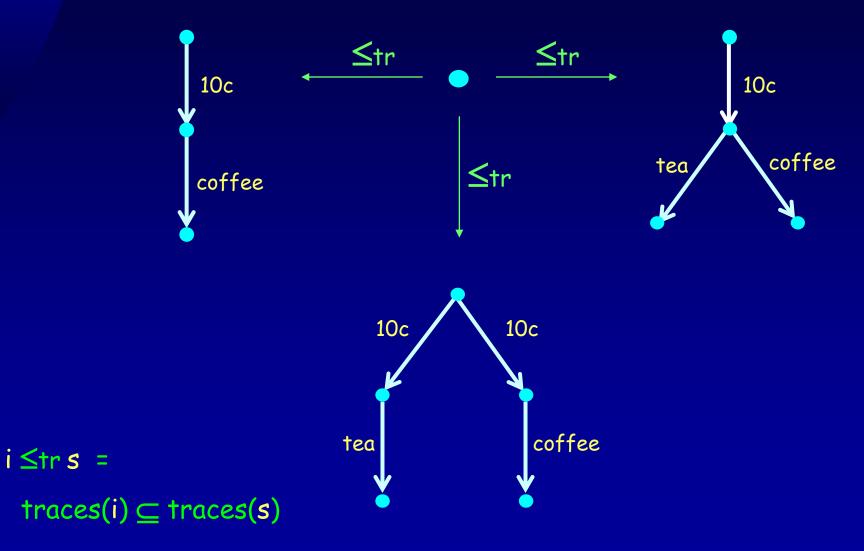


# $i \leq_{tr} S \iff traces(i) \subseteq traces(S)$ Traces: $traces(s) = \{ \sigma \in L^* \mid s \xrightarrow{\sigma} \}$

### Trace Preorder



### Trace Preorder







# Implementation Relation IOCO for Labelled Transition Systems with Inputs and Outputs

### Input-Output Transition Systems

>10c >10c >10c >20c >51 \$52 Icoffee \$52 Itea \$54 10c, 20c

coffee, tea

from user to machine initiative with user machine cannot refuse

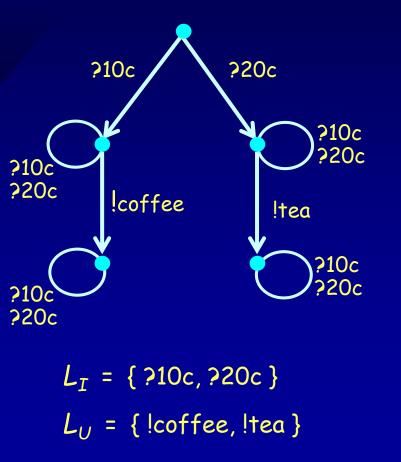
 $L_T \cap L_U = \emptyset$ 

from machine to user initiative with machine user cannot refuse

 $L_T \cup L_U = L$ 

L<sub>I</sub> = { ?10c, ?20c } L<sub>U</sub> = { !coffee, !tea }

### Input-Output Transition Systems

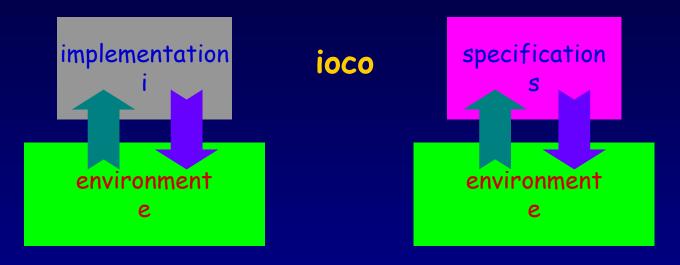


Input-Output Transition Systems IOTS  $(L_{I,L_U}) \subseteq LTS (L_{I,U} \cup L_U)$ 

IOTS is LTS with Input-Output and always enabled inputs:

for all states s, for all inputs  $?a \in L_I$ : s  $\xrightarrow{?a}$ 

# Input-Output Transition Systems with ioco



 $i \in IOTS(L_{I}, L_{U})$   $s \in LTS(L_{I}, L_{U})$ 

#### ioco $\subseteq$ IOTS (L<sub>I</sub>,L<sub>U</sub>) × LTS (L<sub>I</sub>,L<sub>U</sub>)

Observing IOTS where system inputs interact with environment outputs, and v.v.

### **Correctness** Implementation Relation **ioco**

i ioco s =<sub>def</sub>  $\forall \sigma \in Straces(s)$ : out (i after  $\sigma) \subseteq out(s after \sigma)$ 

$$p \xrightarrow{\delta} p = \forall lx \in L_U \cup \{\tau\}, p \xrightarrow{lx}$$
  
Straces (s) = {  $\sigma \in (L \cup \{\delta\})^* | s \xrightarrow{\sigma}$  }  
p after  $\sigma = \{ p' | p \xrightarrow{\sigma} p' \}$   
out (P) = {  $lx \in L_U | p \xrightarrow{lx}, p \in P$  }  $\cup \{\delta | p \xrightarrow{\delta} p, p \in P$  }

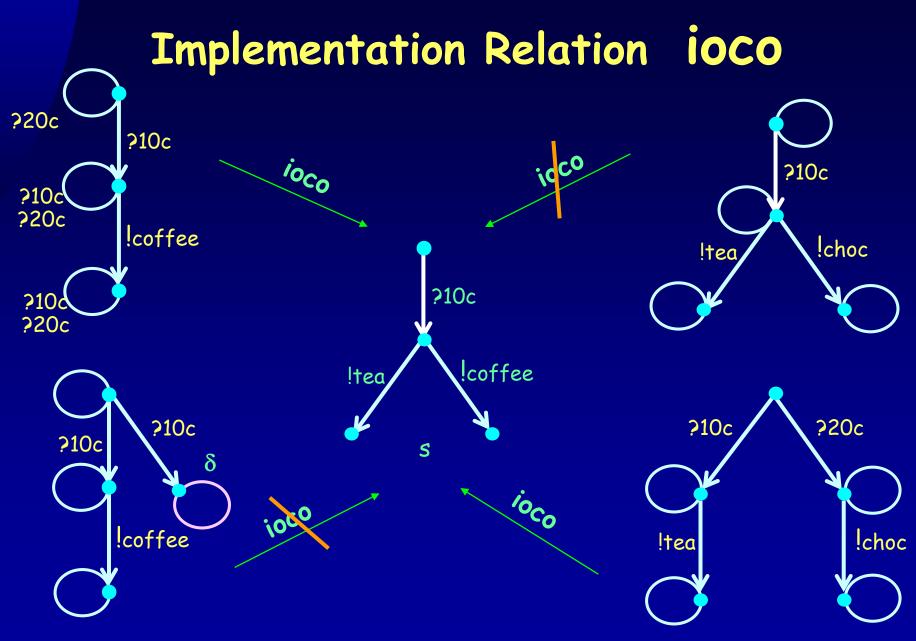
## **Correctness** Implementation Relation **ioco**

i ioco s =<sub>def</sub>  $\forall \sigma \in Straces(s)$ : out (i after  $\sigma) \subseteq out(s after \sigma)$ 

Intuition:

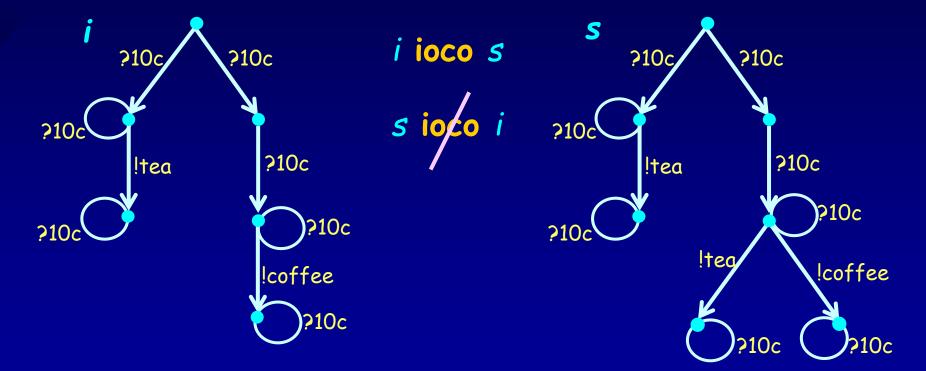
i ioco-conforms to s, iff

- if i produces output  $\times$  after trace  $\sigma$ , then s can produce  $\times$  after  $\sigma$
- if i cannot produce any output after trace  $\sigma_{,}$  then s cannot produce any output after  $\sigma_{}$  ( <code>quiescence \delta</code> )



### Implementation Relation ioco

i ioco s =<sub>def</sub>  $\forall \sigma \in Straces(s)$ : out (i after  $\sigma) \subseteq$  out (s after  $\sigma$ )

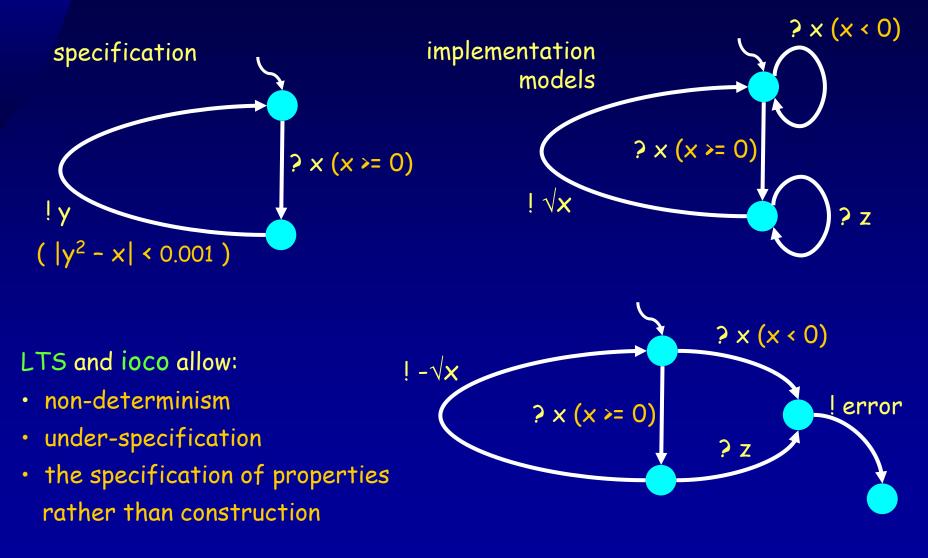


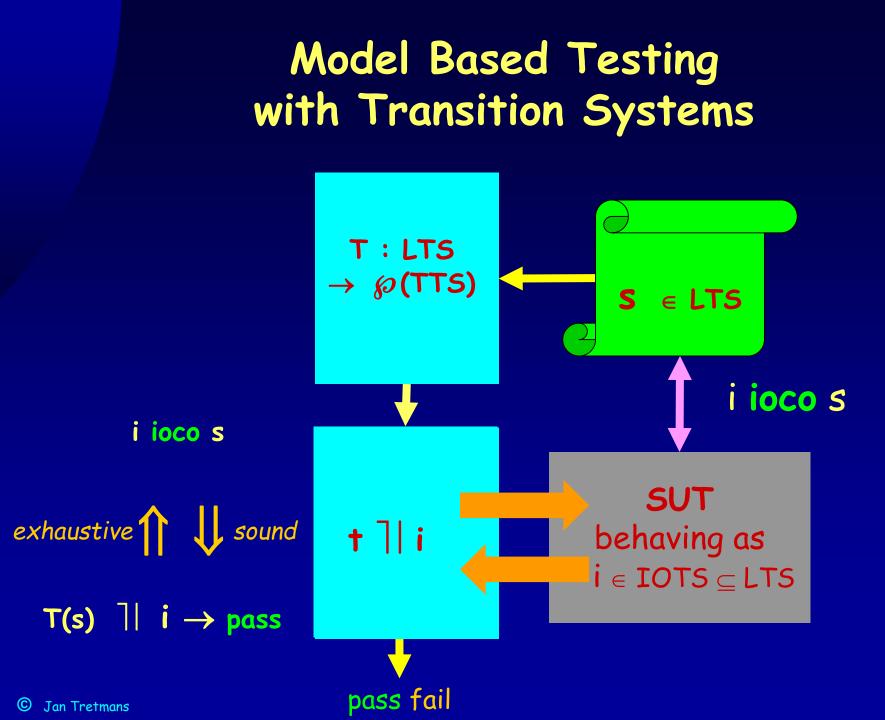
out (*i* after ?10c.?10c) = out (*s* after ?10c.?10c) = { !tea, !coffee }

out (i after ?10c. $\delta$ .?10c) = { !coffee }  $\neq$  out (s after ?10c. $\delta$ .?10c) = { !tea, !coffee }

© Jan Tretmans

#### Implementation Relation ioco





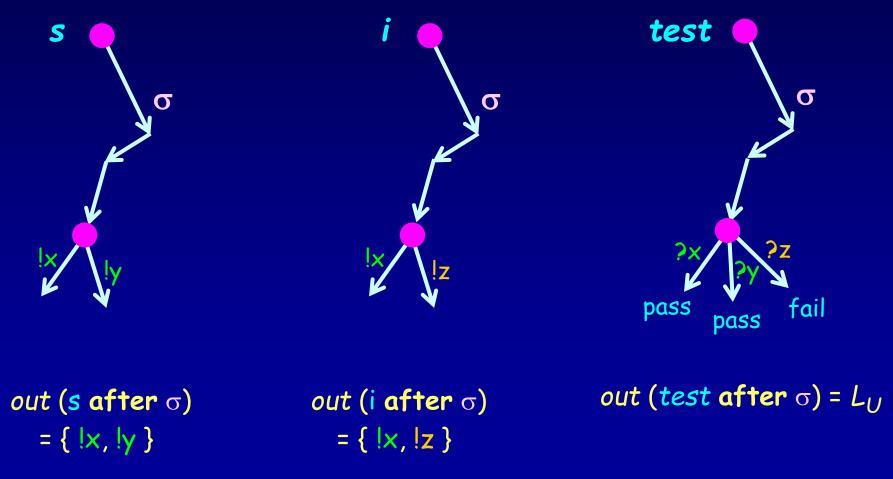




## Test Cases, Test Generation, and Test Execution for Labelled Transition Systems

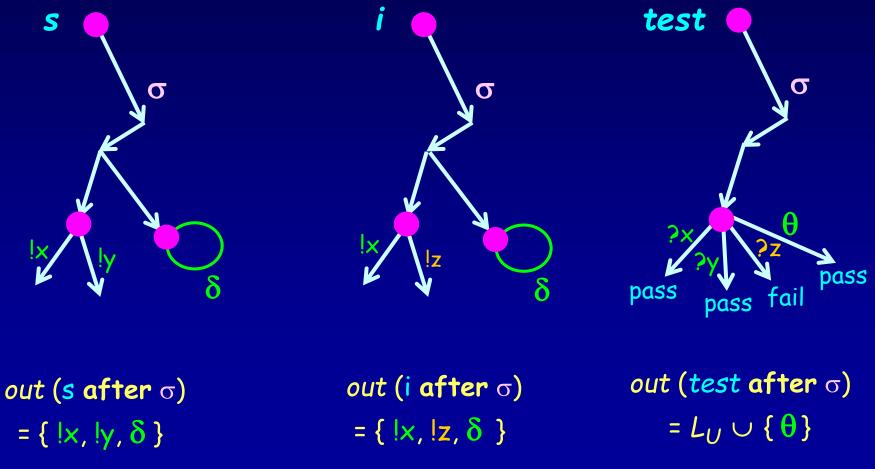
#### **Test Generation**

i ioco s =<sub>def</sub>  $\forall \sigma \in Straces(s)$ : out (i after  $\sigma) \subseteq out(s after \sigma)$ 



#### **Test Generation**

i ioco s =<sub>def</sub>  $\forall \sigma \in Straces(s)$ : out (i after  $\sigma) \subseteq out(s after \sigma)$ 

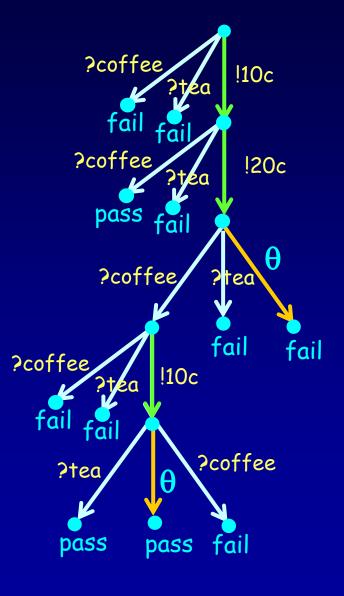


#### Test Cases

#### Model of a test case

- = transition system :
- labels in  $L \cup \{\theta\}$ 
  - 'quiescence' label  $\theta$
- tree-structured
- 'finite', deterministic
- sink states pass and fail
- from each state:
  - either one input la and all outputs ?x
  - or all outputs Px and  $\theta$

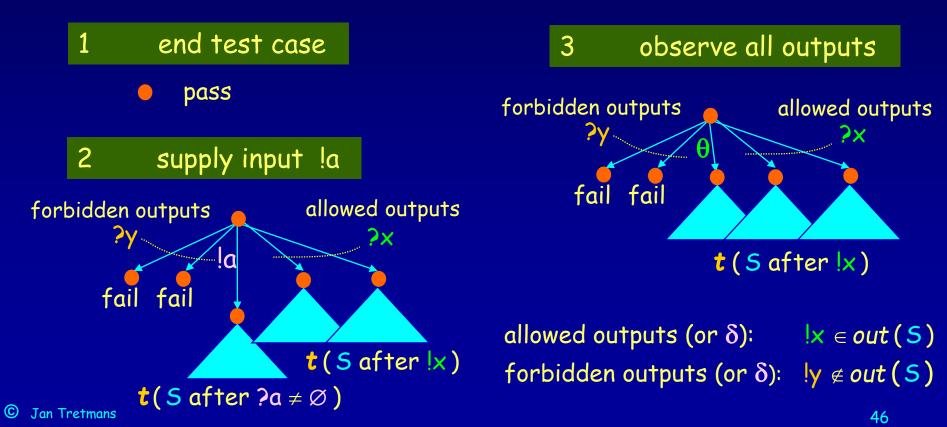




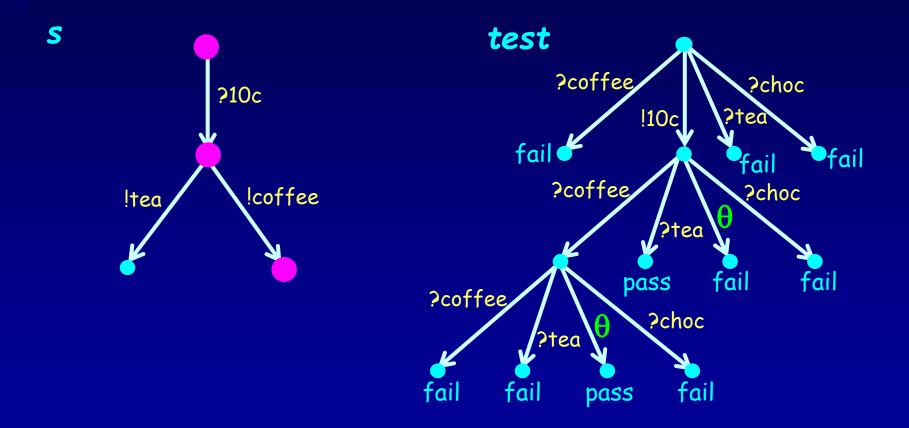
### **Test Generation Algorithm**

#### Algorithm

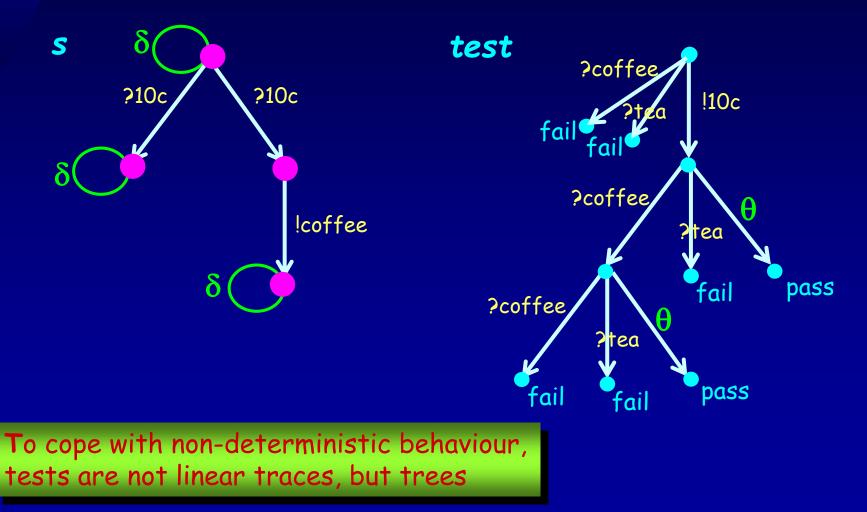
To generate a test case t(S) from a transition system specification S, with  $S \neq \emptyset$ : set of states (initially  $S = s_0$  after  $\varepsilon$ ) Apply the following steps recursively, non-deterministically:



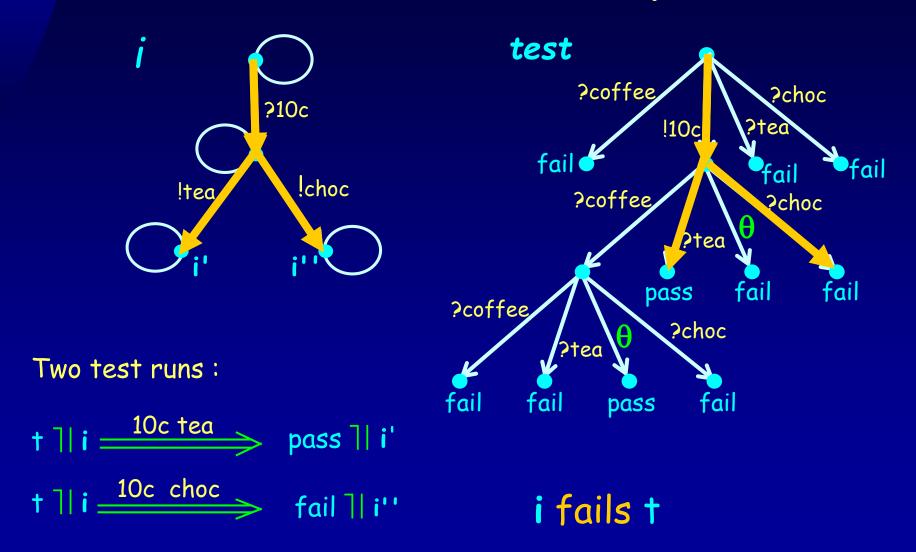
#### **Test Generation Example**

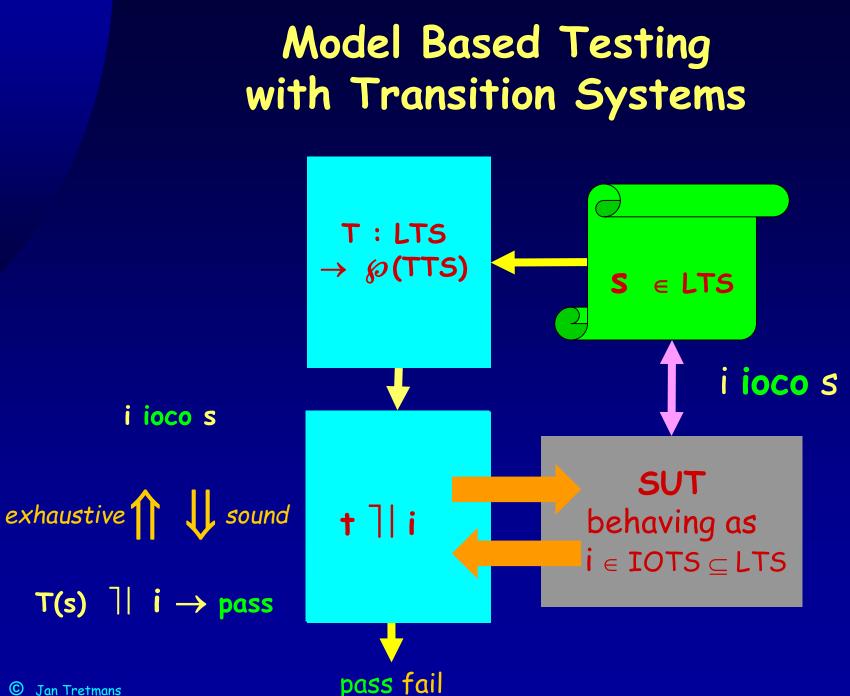


#### **Test Generation Example**



#### **Test Execution Example**



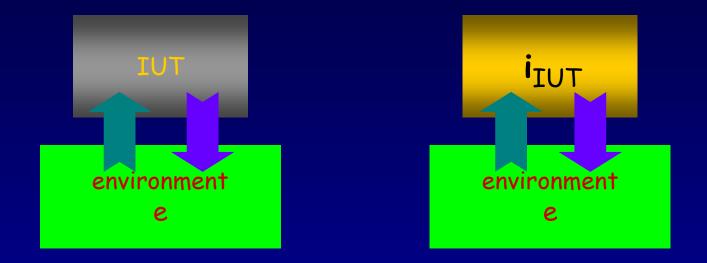






# Testability Assumption (Test Hypothesis)

#### Comparing Transition Systems: An Implementation and a Model

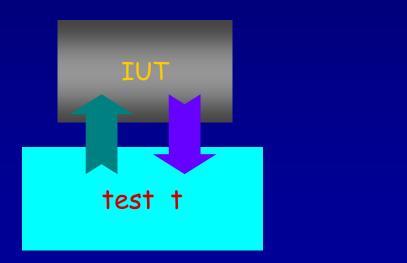


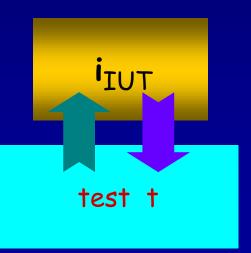
#### $IUT \approx i_{IUT} \Leftrightarrow \forall e \in E . obs(e, IUT) = obs(e, i_{IUT})$

#### Formal Testing : Test Assumption

Test assumption :

 $\forall$  IUT.  $\exists$   $i_{IUT} \in MOD$ .  $\forall$   $t \in TEST$ . IUT passes  $t \iff i_{IUT}$  passes t









## Soundness and Exhaustiveness

55

## Validity of Test Generation

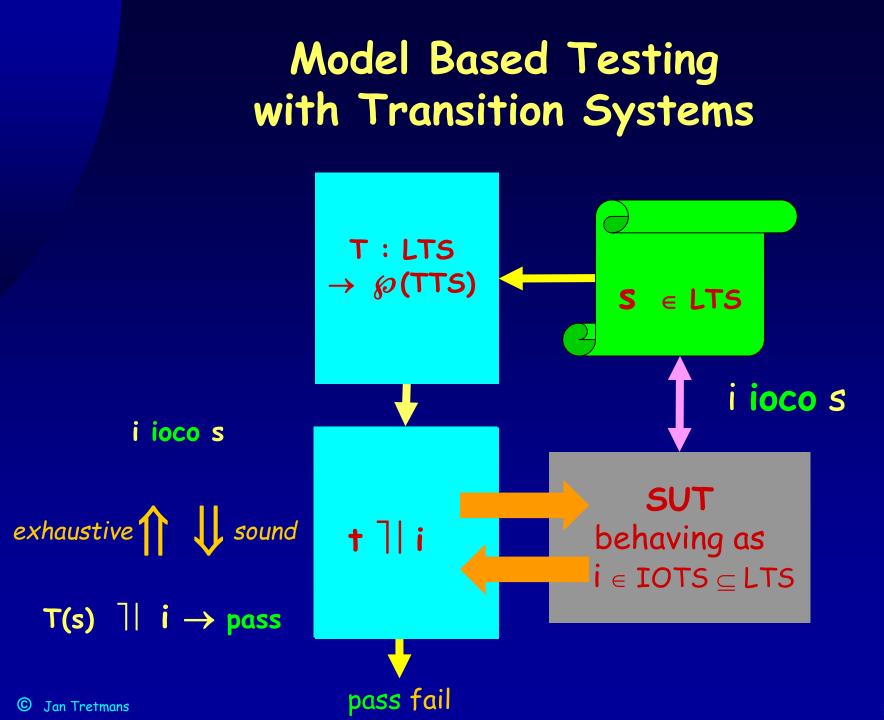
For every test t generated with algorithm we have:

Soundness:
 t will never fail with correct implementation

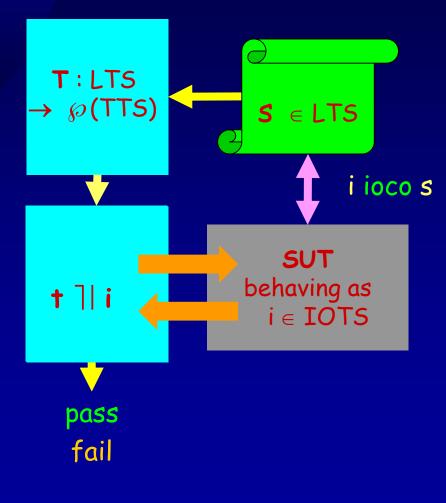
i ioco s implies i passes t

Exhaustiveness:

each incorrect implementation can be detected with a generated test t



#### The ioco Theory for Model-Based Testing



Test assumption :  $\forall IUT \in IMP . \exists i_{IUT} \in IOTS .$   $\forall t \in TEST. IUT$  passes t  $\Leftrightarrow i_{IUT}$  passes t

Proof soundness and exhaustiveness: ∀i∈IOTS . (∀t∈ T(s).ipasses t) ⇔ i ioco s

SUT ioco s exhaustive ↑ ↓ sound SUT ↑ T(s) → pass